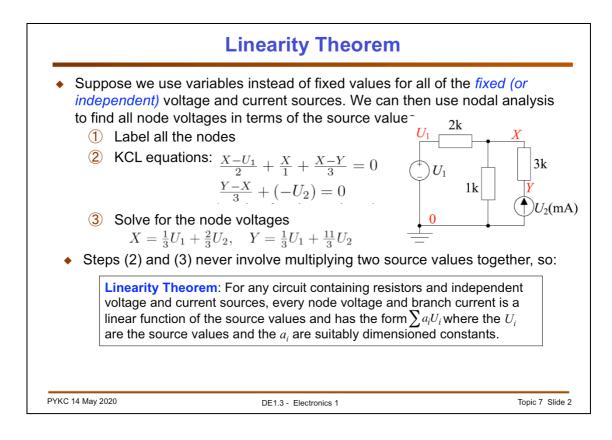


In this lecture, we will develop three important concepts relating to electronic circuits: 1) The concept of LINEARITY; 2) the concept of SUPERPOSITION; 3) The idea of equivalent circuits.

I will approach these from a general point of view and then show you how these two concept may be used in understanding and working with electronic circuits, or indeed, many engineering systems.



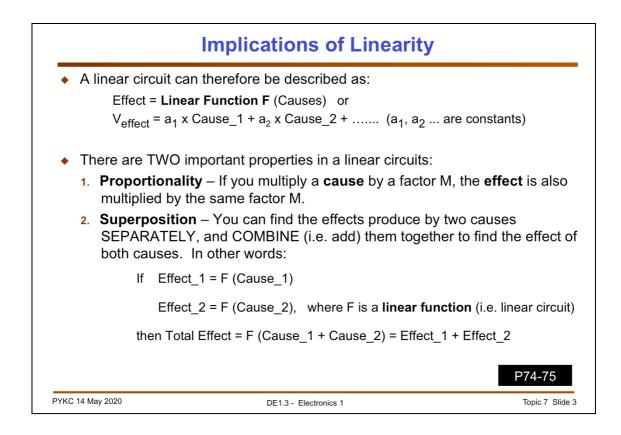
Let us take one of the circuits we have seen before, and label the voltage and current sources with variables U1 and U2.

Apply KCL at node X and Y. You can now solve for unknowns X and Y in terms of U1 and U2.

Since U1 and U2 are energy **sources**, they are the "**causes**" of voltages at X and Y. These voltages are the "**effect**".

What the equation in step 3 shows is that X and Y are always **WEIGHTED SUMS** of the sources. The equations shown here hold LINEAR relationship between the causes (U1, U2) and the effects (X, Y).

We say that this circuit is a **LINEAR** circuit because all node voltages and branch currents are linearly related to the sources.

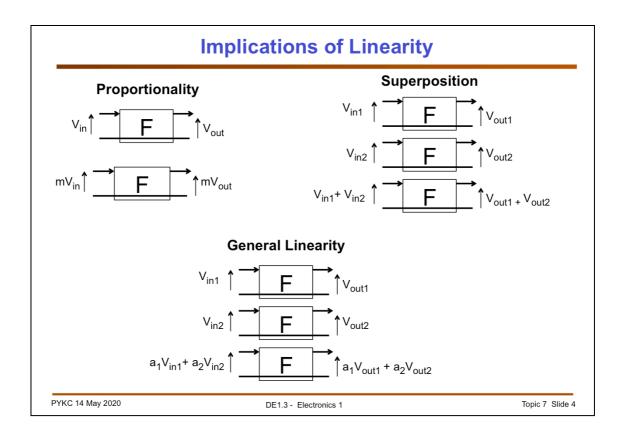


So, linearity is characterised by a linear function which is a weighted sum of variables (causes) where the weights are constants.

Linearity in a system results in two important properties. They are: 1) **Proportionality**, and 2) **Superposition**.

Proportionality means that if you double to cause, the effect also doubles.

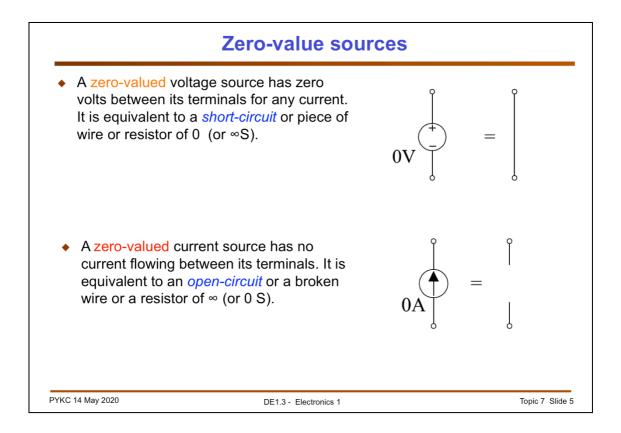
Superposition means a the combined effect due to two (or more) causes is the sum of the effects for each cause taken separately.



This is best shown diagrammatically. Although what is shown here are input and output voltages to a system, the same holds true for any node voltages or currents inside a circuit, and for any fixed or dependent sources (provided that the defining equation of the dependent sources also has a linear relationship).

Combining the **proportionality** and the **superposition** principles together give us the general **linearity property**, which is modelled by the weighted sum equation:

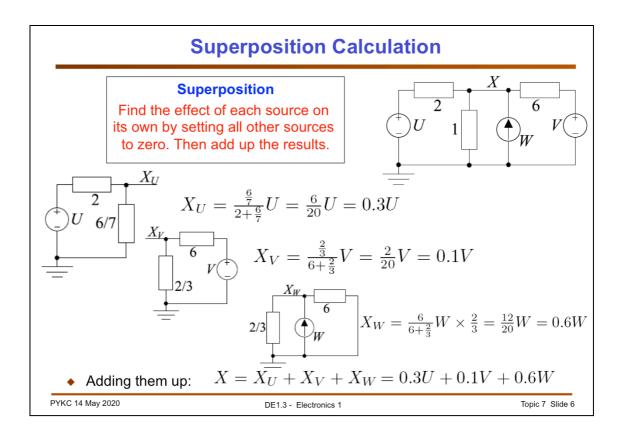
 $X = \Sigma a_i U_i$



Let me further introduce the idea of zero-value sources. A voltage source could have a zero value. In which case, it is equivalent to a short circuit or a wire. If you have a voltage source with a fixed value V_S , and if you wish to "eliminate" its impact (cause) on a circuit, you can regard that $V_S=0$, or replacing the voltage source with a wire.

If a current source gives 0A, it is equivalent to an OPEN CIRCUIT (with infinite resistance). If you have a current source with a fixed value I_S , and if you wish to "eliminate" its impact (cause) on a circuit, you can regard that I_S =0, or removing the current source from the circuit all together.

Why are these two concept useful? You will see later.



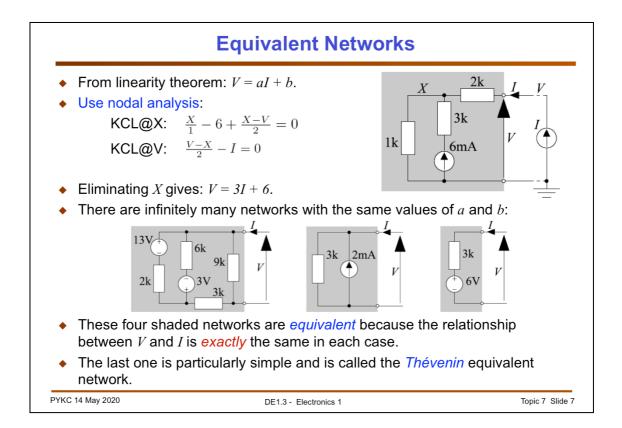
Let us apply the principle of superposition to evaluation X in the circuit shown here.

Step 1: Consider only source U and zero both V and W. Zero V means replacing it with a wire. Zero W mean removing W altogether. This results in resistor 1 || 6 = 6/7. Therefore X_U = 0.3U.

Step 2: Consider only source V and zero both U and W. We short U to ground and remove W. This results in resistor 2 || 1 = 2/3. Therefore $X_V = 0.1V$.

Step 3: Consider only source W and zero both U and V. We short both U and V. Therefore $X_W = 0.6W$.

Using superposition $X_{total} = 0.3U + 0.1V + 0.6W$



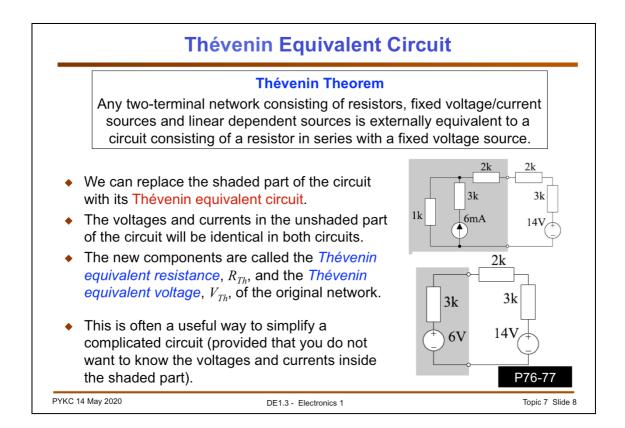
Let us consider the circuit shown here. Imagine that circuit in gray is a "**black box**", and we are interested in the relationship between the voltage V at the output of the black box, and the current I flowing into the black box. We can model this situation with the unknown current source I at the output of the black box.

From slide 8 of this lecture, we know that since there is only one unknown source I, By applying linearity theorem, we get V = aI + b. Perform KCL at X and V, we get V = 3I + 6.

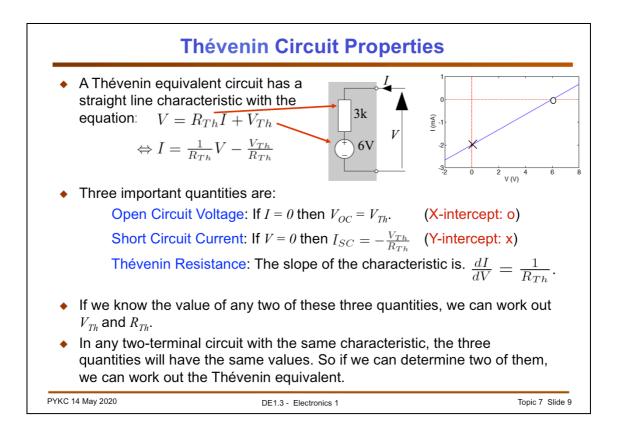
Interestingly, we can replace the gray circuit (the black box) with many different circuits that have exactly the same V-I relationship.

Shown below are three alternatives which are equivalent to the circuit on the top. They are equivalent because they all have the same V vs I equation.

The right-most circuit, which could replace all the other equivalent circuit is simple and most important, and it is known as the **Thévenin equivalent** circuit.



Thévenin Theorem states: consider a two terminal circuit (network) with only resistors, fixed sources and linear dependent sources. No matter how complex this circuit is, you can always replace it with a simple fixed voltage source in series with a resistor. So the upper shaded circuit can be replaced with the lower shaded circuit.



Thévenin equivalent circuit always have a straight line characteristic. (Characteristic of a component or an electronic widget is the plot between I-V or V-I, or some other electrical parameters). In a Thévenin equivalent circuit,

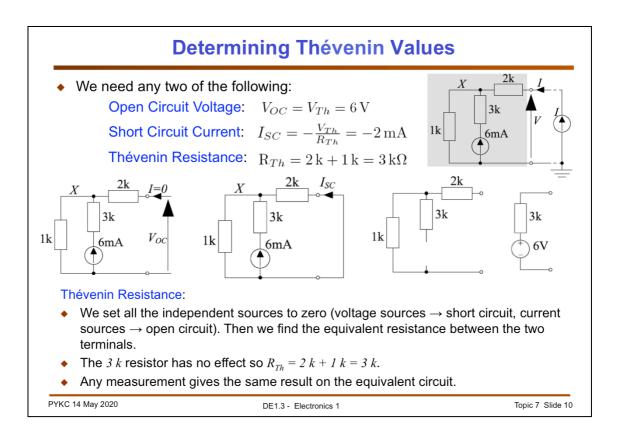
 $V = R_{TH} I + V_{TH}$

 V_{TH} = Thévenin voltage, R_{TH} = Thévenin Resistance.

The Thévenin voltage can be found by open circuit (i.e. not connect anything) the shaded circuit. Then I = 0, and V = V_{TH} .

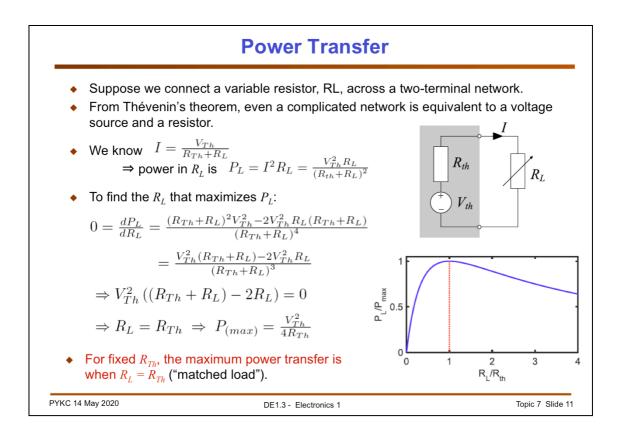
 R_{TH} , is simply the 1/ gradient of the curve, or 1/ dI/dV.

Alternative, you can think of R_{TH} as the resistance of the shaded circuit when V_{TH} is zero.



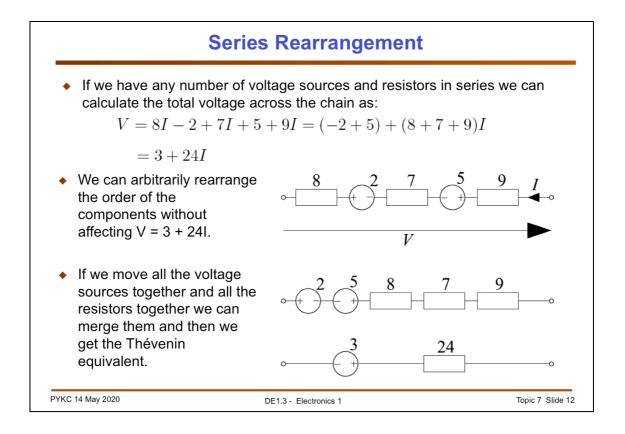
Let us consider how to go from the complicated circuit on the upper right to the simple Thévenin equivalent circuit on the lower right. Remember,

- Step 1: open circuit the output (i.e. I = 0). Find V_{OC} .
- Step 2: we short circuit the output (i.e. V = 0), and find the current I.
- Step 3: we zero the source (i.e. the 6mA source become 0mA) and in this case, the current source is removed. Then compute the resistance between the two terminals.



Let us consider a source with some internal resistance modelled by the Thévenin equivalent circuit as shown here. What is value is the resistor load such that the power transferred from the source to the load resistor is MAXIMUM?

The calculation here shows that maximum power is transferred if the load RL matches that of the Thévenin resistance. $\rm R_{TH}.~$ This is known as Thévenin Thévenin .



Here are some examples of how to re—arrange sources and resistances to simply circuits.

Summary

- Linearity Theorem: $X = \sum_{i} a_i U_i$ 'er all independent sources U_i
- Proportionality: multiplying all sources by k multiplies all voltages and currents by k and all powers by k².
- Superposition: sometimes simpler than nodal analysis, often more insight.
 - Zero-value voltage and current sources
- If all sources are fixed except for U₁ then all voltages and currents in the circuit have the form aU₁ + b.
- Power does not obey superposition.
- Thévenin Equivalent Circuits
 - How to determine V_{Th} , I_{NO} and R_{Th}
 - ► Method 1: Nodal analysis
 - ► Method 2: Find any two of $V_{OC} = V_{Th}$, I_{SC}
 - > R_{Th} is the equivalent resistance with all sources set to zero
 - Ohm's law is satisfied: $V_{Th} = I_{NO}R_{Th}$
 - Load resistor for maximum power transfer = R_{Th}

PYKC 14 May 2020

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Topic 7 Slide 13